

Department of Electronics & Communication Engineering

Guru Ghasidas Vishwavidyalaya, Bilaspur, 495009 (C.G.) (A Central University established by the Central Universities Act, 2009)

Date: 27/04/2023

Counseling cum Interview for the Admission in PhD program (session 2022-23)

All VRET qualified candidates are hereby called for interview and presentation in the department of Electronics & Communication Engineering.

Tentative Date of Interview: 12th May, 2023 (Friday)

Time of Interview: 11.00 AM

Mode of Interview: Blended mode

Venue: Office, Department of Electronics & Communication Engineering

The physical presence of the candidate is highly expected however; in case of genuine reasons the candidate may appear online by taking prior permission from Head of the Department at least three days before the date of interview. No TA/DA will be paid to the candidates for interview. It will be the responsibility of the candidate to make proper arrangement for online presentation during the interview.

The provisional list of VRET qualified candidates are given below:

SN	Name	Father Name	Category	Roll No	Marks obtained
1	Priyanka Gupta	Hari Ram	OBC/EWS	1260005	69
2	Pratibha Sahu	Chitresh Kumar Sahu	OBC	1260004	66
3	Binanda Kishore Mondal	Bankin Kishore Mondal	SC	1260001	64
4	Gargi Mishra	Prasun Kumar Mishra	GEN	1260003	63

Instructions to the candidates:

- 1. The candidates have to bring the following list of documents with two sets of Photocopies along with original documents:
- (i) 10th (HSC) mark sheet (Mandatory)
- (ii) 12th (HSSC) mark sheet (Mandatory)
- (iii) UG marks sheet (Mandatory)
- (iv) PG marks sheet (Mandatory)
- (v) Transfer certificate/College leaving certificate (optional), if available
- (vi) Caste/Category/PwD certificate, if any (Mandatory if applied under any category)
- (vii) Valid Income certificate for OBC issued by competent authority, if applicable
- (viii) Valid EWS certificate issued by competent authority, if applicable
- 2. The candidates who want to appear in online interview must take prior approval from Head, Department of Electronics & Communication Engg., GGV at least three days before with valid reasons.
- 3. All candidates must submit the proposal of their write up of research interest/plan (maximum two pages) to the HoD by 9th May, 2023.
- 4. All candidates have to make a power point presentation (10-15 slides) of their research proposal and have to present it within 10 minutes and then 5 minutes for discussion (Total 15 minutes) on the day of interview.
- 5. The candidates are advised to read carefully all the instructions given by the University uploaded on website regarding PhD admission including OM ref No. 40/Academic/2023 dated 21 April 2023.

