

## Details of Equipments/Instruments

<b>Part A</b>			
	1.	Laboratories Name/Location	VLSI LAB / Room number F-5
	2.	Name of the equipment	Xilinx Spartan 3E FPGA Trainer (VPTB 10)
	3.	Equipment ID	GGV/ECE/2012/1-6/VLSI
	4.	Model Number/	VPTB 10
	5.	Technical Specification:	---
	6.	Equipment Description/Application:	For the burn of Verilog/VHDL code
	7	Stock Register Page No	119

<b>Part B</b>			
<b>1.</b>	<b>Details of Purchase-</b>		
	I.	Name of the instrument/Facility:	Xilinx Spartan 3E FPGA Trainer (VPTB 10)
	II.	Sponsored/Funded by:	University
	III.	Cost of the Instrument:	56010
	IV.	Date of Purchase:	16/11/2012
	V.	Whether it is under AMC:	No
	VI	Name of Incharge:	Dr.Sudakar Singh Chauhan& Mr. ChandanTamrakar
	VI	Status of Instrument at present:	Working
<b>2.</b>	<b>User manual(Application related Area)-</b>		
	I.	Target User (in house/ other Institutions):	In house
	II.	Method of sharing of instruments:	NA
	III.	Fee details (if, paid service, any):	NA
<b>3.</b>	<b>Up to date Utilization of facility (Utilization report to be uploaded every 6 month):</b>		
	Yes		
<b>4.</b>	<b>Output&amp; the activity performed (In terms of publications/patents/Ph.D./Master Dissertations etc.):</b>		
	The project/Lab/research work for B.Tech., M. Tech. & Research students.		

