

## Department of Pure & Applied Physics Guru Ghasidas Vishwavidyalaya, Bilaspur (C.G.) India

(A central University Established by the Central Universities Act 2009 No. 25 of 2009)

## **CERTIFICATE**

This is to certify that VIDYASHREE SHRIVASTAVA has carried out the project on the topic "DIODE-TRANSISTOR LOGIC" in the Department of Pure And Applied Physics, GURU GHASIDAS VISHWAVIDYALAYA, BILASPUR under my supervision. She has worked diligently, meticulously and methodically and collected the literature very sincerely and carefully. During this project work She has learnt few of analytical technique of communication & modulation. To the best of our knowledge the work presented in this project is original and has not been submitted anywhere. I recommended the project report to be forwarded to the respective examiners for evaluation. I wish her all success in her life & darcer.

विभागाध्यक्ष/H.O.D. व्ह एवं अनुप्रयुक्त भौतिकी विभाग

SUBMITTED BY: Dept. of

SUPERVISED BY:

VIDYASHREE SHRIVASHAV(A.T.)/Bilaspur (C.G.)R. SHALINTA TIGGA MA'AM

B.se. electronics VI sem.

Department of Pure & Applied
Physics GGV, BILASPUR(CG)

ASSISTANT PROFESSOR Department of Pure & Applied Physics GGV, BILASPUR, (C.G.)