

# **Certificate**

This is to be certified that pratiksha sahu student of B.Ac VI Semester, Department Of PURE AND APPLIED PHYSICS, Guru Ghasidas Vishwavidyalaya Bilaspur , have completed his project entitled logic gates using transistor. She has submitted his project report for the partial fulfillment of the curriculum of the Degree of Bachelor of Science (VI Sem) from GGV. During this project work she has learned about the 555 timer IC, resistor ,LDR ,Relay and its application to the best power knowledge. I recommend the project report to be forwarded for evaluation. I wish him all success in his life and career.

Date:30.04.2022.....

**PROF. S.P.PATEL SIR**  
**ASISTANT PROFESSER**

**DEPT. OF PURE & APPLIED PHYSICS**

**GGV, BILASPUR CHHATTISGARH**