Guru Ghasidas Vishwavidyal aya Bil aspur (C.G.)

(A Central university Established by the central universities acts 2009 No. 25 of 2009)

<u>CERTIFICATE</u>

This is a certify that thesis entitled "BCD TO 7-SEGMENT DECODER DISPLAY CIRCUIT" submitted by "Mr. RAMESH KUMAR SAHU" in the partial fulfillment for the requirements for the award of Bachelor of Science Degree in Electronics at Department of Pure and Applied Physics, Guru Ghasidas Vishwavidyalaya (A central university) Bil aspur (C.G.) 495001, INDIA is an authentic work carried out by film under my supervision and guidance.

To the best of my knowledge, the matter embodied in the thesis has not been submitted to any other University/Institute for the award of Degree or Diploma.

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